

INFORMATION DISCLOSURE CITATION

(USE SEVERAL SHEETS IF NECESSARY)

ATTY DOCKET NO.
POU920030027US1

SERIAL NO.

APPLICANT(S)
BRICE ET AL.

FILING DATE
HEREWITH

GROUP

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,170,472	12/08/92	Cwiakala et al.	395	275	
	AB	5,414,851	05/09/95	Brice, Jr. et al.	395	650	
	AC	5,452,455	09/19/95	Brown et al.	395	700	
	AD	5,568,648	10/22/96	Coscarella et al.	395	862	
	AE	5,600,805	02/04/97	Fredericks et al.	395	825	
	AF	5,644,712	07/01/97	Coscarella et al.	395	200.01	
	AG	5,845,146	12/01/98	Onondera	395	822	
	AH	5,907,684	05/25/99	Halma et al.	395	200.67	
	AI	5,996,026	11/30/99	Onondera et al.	710	3	
	AJ	6,167,459	12/26/00	Beardsley et al.	710	3	
	AK	6,170,023 B1	01/02/01	Beardsley et al.	710	36	
	AL	6,185,638 B1	02/06/01	Beardsley et al.	710	36	
	AM	6,188,702 B1	02/13/01	Tornetta et al.	370	535	
	AN	6,240,467 B1	05/29/01	Beardsley et al.	710	5	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
							NO	YES	ABSTRACT

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	U.S. Patent Application Serial No. 10/436,021, Brice et al., "Multiple Logical Input/Output Subsystem Facility," filed May 12, 2003.	
AP	U.S. Patent Application Serial No. 10/435,955, Brice et al. "Sharing Communications Adapters Across A Plurality Of Input/Output Subsystem Images," filed May 12, 2003,	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial here if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. **Include copy of this form with next communication to applicant.**